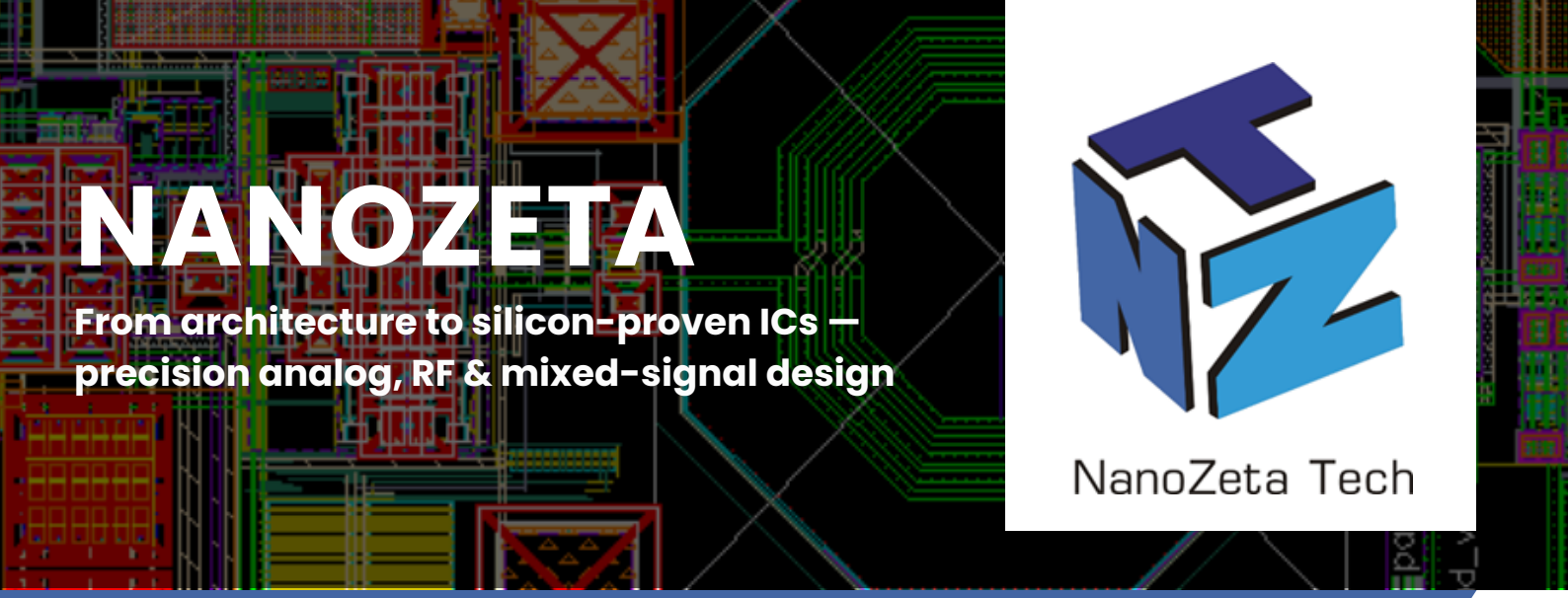

NANOZETA

From architecture to silicon-proven ICs — precision analog, RF & mixed-signal design

NanoZeta Tech

Who we are

NanoZeta Technologies is a specialized IC design house focused on analog, RF, and mixed-signal integrated circuits.

With dozens of successful RF/mmWave and mixed-signal tape-outs, we deliver production-ready designs through complete modeling, simulation, layout, and lab validation.

Working from architecture definition to silicon correlation, our team ensures close agreement between simulations and measurements.

NanoZeta combines deep circuit expertise with system-level insight to design PLLs, frequency synthesizers, RF front ends, analog basebands, and sensor interfaces tailored to each client.

150+ years of collective experience

>95% first-pass success

20+ sucessful tape-outs

Contact & locations

CYPRUS

59 Griva Digeni Ave. (Kaimakliotis Building, Office 401), CY-6043 Larnaca

GREECE

166A Kifissias Ave., 15126 Athens

ISRAEL

5 Tarpav St., 5250381 Ramat Gan

USA

P.O. Box 5, 95031 Los Gatos, CA

www.nanozetatech.com

athens@nanozetatech.com

iris@icleviathan.com (Iris Segev is our authorized Sales Rep in Israel)

cy.linkedin.com/company/nanozetatech

Our experience

NanoZeta Tech

NanoZeta Technologies has developed a broad portfolio of silicon-proven circuits and systems, built with clients and research partners. These designs form the foundation of our know-how and reusable reference blocks for new custom ICs. Our experience spans a wide range of analog, RF, and mixed-signal subsystems, including:

- 17-20GHz sub-200fs jitter Frac-N PLL for high speed SerDes in TSMC 5nm (silicon-proven)
- Automotive G1, ultra-low noise, 10-GHz, Frac-N frequency synthesizer for very precise linear chirp generation in GF 22FDX (silicon-proven)
- 2.4GHz BLE transceiver (silicon-proven)
- 60GHz Radar-on-Chip consisting of 8 x RX, 2 x TX, Analog baseband, and PLL (silicon-proven)
- 76-81 GHz receiver-RFFE and analog baseband (silicon-proven)
- Op-amps, level shifters, temperature sensors, SPI, BGRs, LDOs (mass-production-proven)
- Low-Noise Amplifiers up to 80GHz (silicon-proven)
- Down- & up-converters, up to 80GHz (silicon-proven)
- 96MHz Integer-N PLL, 24.576MHz-384MHz Integer-N PLL, 480MHz Integer-N PLL, 528MHz Frac-N PLL, 20GHz Integer-N PLL, 13.5-16.5 GHz Frac-N PLL (mass-production-proven)
- 57-66GHz linear PA and 2.45GHz switched mode PA (silicon-proven)
- 14-bit SAR ADC, 2MS/s (silicon-proven)

Our services

Full IC design or Subsystem Design

From architecture to verified silicon: PLLs up to hundreds of GHz, RF/mmWave transceivers, analog basebands, and mixed-signal systems.

We design in bulk CMOS, FD-SOI (down to 22 nm), FinFET (down to 5 nm), and BiCMOS, with full EM extraction and layout/schematic co-design from early stages.

Integration support includes mmWave antenna design (in-package or on PCB) and co-design flow between silicon and packaging to optimize performance.

Feasibility Study Service for Clock Generators (PLLs, DLLs, XOs)

Light (1 month):

- Designed for early feasibility assessment, it verifies if target requirements are achievable in silicon and provides data for internal decision-making or investor presentations.
- Includes system level simulations, architecture proposal, phase noise / consumption / spur trade-off report, Risk analysis and proposal

Plus (2-3 months):

- Critical systems preliminary schematic design and deeper analysis

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