



# Precision IC design: From feasibility to verified silicon

NanoZeta Technologies takes ICs from concept to production with tight simulation-to-silicon alignment and predictable time-to-market.

**Core expertise:**

- PLLs & frequency synthesis
- RF/mmWave front-ends
- Advanced mixed-signal subsystems

150+

years of collective experience

>95%

first-silicon success

20+

successful tape-outs

## Full IC design or Subsystem Design

From idea to silicon — we design high-performance PLLs, RF/mmWave transceivers, and mixed-signal front ends tailored to your specifications

### WHAT WE OFFER

- **Architecture & modeling:** System-level and behavioral models to identify risks early and guide key design decisions.
- **Schematic & layout co-design:** CMOS, FD-SOI, FinFET, or BiCMOS implementations with early EM extraction and parasitic-driven optimization.
- **EM-accurate layout & verification:** Full-chip EM, physical verification, and extensive simulations for tight silicon-to-simulation correlation.
- **Silicon bring-up support:** Measurement-protocol design and lab assistance for smooth integration.
- **Optional antenna/PCB co-design** for RF/mmWave systems.

### OUR APPROACH

Early architectural modeling, schematic–layout co-development, and EM-based verification ensure predictable performance, high yield, and first-silicon accuracy.





NanoZeta Tech

OUR SERVICES

# Feasibility Study Service for Clock Generators *specialized for PLLs, DLLs, XOs*

## Feasibility Light

1 month

- Target specification and architecture proposal
- Behavioral modeling and system level simulations (expected phase noise, spur, dynamics)
- Power, area and jitter trade-off analysis
- Peripheral and interface requirement review (regulators, filters and digital interconnect)
- Risk and recommendation report ready for planning or investor use

***Ideal for quick proof of feasibility and cost estimation***

## Feasibility Plus

2-3 months

- All deliverables from Feasibility Light
- Preliminary schematic design for critical subsystems (VCO, CP, etc)
- Updated noise/jitter and current estimates from simulations
- Refined peripheral analysis and integration recommendations

## ENGAGEMENT MODEL

We integrate with our clients' teams to co-design the systems you need. Through weekly calls, transparent communication, and detailed documentation, we ensure full alignment and adaptability throughout real-project development

## Contact & locations

### CYPRUS

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